

WarCraft 3 - Reign Of Chaos The Frozen Throne V.1.26a No-cd Crack.rar.rar

The screenshot displays the MPLAB Code Configurator interface. The central pane shows the ADC hardware settings, including the 'Easy Setup' tab, 'Hardware Settings' section, and 'Selected Channels' table. The 'ADC Clock' section is configured with 'FOSC/2' as the clock source and an acquisition time of 2. The 'Selected Channels' table lists the internal channel and RA0 pin.

| Pin | Channel | Custom Name |
|------------------|---------|-------------|
| Internal Channel | FVR | channel_FVR |
| RA0 | AN0 | POT |

The right pane shows the 'MPLAB Code Configurator Pin Manager' for the PIC18F26K20, listing pins RA0 through RC3 and their corresponding functions. Below the pin manager is an 'Export' button and a table showing the package configuration for SOIC28.

| Package: | SOIC28 | Pin No: | 2 | 3 | 4 | 5 | 6 | 7 | |
|----------|----------|-----------|----------|---|---|---|---|---|--|
| | | | PORT A ▼ | | | | | | |
| Module | Function | Direction | 0 | 1 | 2 | 3 | 4 | 5 | |
| ADC | ANx | input | 🔒 | 🔒 | 🔒 | 🔒 | | 🔒 | |
| ADC | VREF+ | input | | | | 🔒 | | | |
| ADC | VREF- | input | | | | 🔒 | | | |

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